

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		09/608,624	JOURDAN ET AL.	
		Examiner	Art Unit	Page 1 of 1
		Henry W.H. Tsai	2183	

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,507,921	01-2003	Buser et al.	714/45
	B	US-6,279,103	08-2001	Warren, Robert	712/227
	C	US-6,233,678	05-2001	Bala, Vasanth	712/240
	D	US-6,216,200	04-2001	Yeager, Kenneth	711/100
	E	US-6,189,140	02-2001	Madduri, Venkateswara Rao	717/128
	F	US-6,185,675	02-2001	Kranich et al.	712/238
	G	US-5,889,999	03-1999	Breternitz et al.	717/158
	H	US-5,461,699	10-1995	Arbabi et al.	706/21
	I	US-4,575,814	03-1986	Brooks et al.	711/217
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Black et al., "The Block-base Trace Cache", ACM SIGARCH Computer Architecture News, Proceedings of the 26 th annual international symposium on computer architecture, May 1999.
	V	Patel et al., "Improving Trace Cache Effectiveness with Branch Promotion and Trace Packing", ACM SIGARCH Computer Architecture News, Proceedings of the 25th annual international symposium on computer architecture, April 1998.
	W	Frinly et al., "Alternative Fetch and Issue Policies for the Trace Cache Fetch Mechanism", Proceedings of the 30th annual ACM/IEEE international symposium on Microarchitecture, December 1997.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.